



IPW

Docket No.: M4065.0100/P100-B  
(PATENT)

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Patent Application of:  
Howard E. Rhodes

Allowed: December 17, 2004

Application No.: 10/617,706

Confirmation No.: 3719

Filed: July 14, 2003

Art Unit: 2814

For: CMOS IMAGER WITH  
SELECTIVELY SILICIDED GATES

Examiner: T. N. Quach

**RESPONSE TO NOTICE REGARDING DRAWINGS**

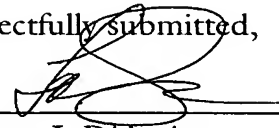
MS Issue Fee  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

In response to the Notice Regarding Drawings dated April 11, 2005, applicant respectfully submits a replacement sheet containing FIGS. 15-17. The replacement sheet corrects the informality listed on the Notice of Draftsperson's Patent Drawing Review. Please replace the previously filed drawing with the corrected drawing and pass the application to issue.

Dated: June 2, 2005

Respectfully submitted,

By 

Thomas J. D'Amico

Registration No.: 28,371

Elizabeth Parsons

Registration No.: 52,499

DICKSTEIN SHAPIRO MORIN &  
OSHINSKY LLP

2101 L Street NW

Washington, DC 20037-1526

(202) 785-9700

Attorneys for Applicant